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| APPL NO.   | FILING OR 371<br>(c) DATE | ART UNIT | FIL FEE REC'D | ATTY. DOCKET NO | DRAWINGS | TOT CLMS | IND CLMS |
|------------|---------------------------|----------|---------------|-----------------|----------|----------|----------|
| 10/630,568 | 07/29/2003                | 2818     | 1044          | 299002056900    | 18       | 27       | 5        |

OCT 27 2003

CONFIRMATION NO. 8646

25226

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 755 PAGE MILL RD Palo Alto  
 PALO ALTO, CA 94304-1018

## FILING RECEIPT



\*OC000000011094618\*

Date Mailed: 10/24/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2002-221311 07/30/2002

If Required, Foreign Filing License Granted: 10/23/2003

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

## Title

Reading circuit, reference circuit, and semiconductor memory device

DOCKETED

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as Express Mail, Airbill No. EV 332780748 US, in an envelope addressed to: MS Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated:

11/6/02

Signature:

(Lamara Alcaraz)

Docket No.: 299002056900

Client Ref.: F5-03036333/03R00455/US/JOH  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Yasumichi MORI et al.

Application No.: 10/630,568

Confirmation No.: 8646

Filed: July 29, 2003

Art Unit: 2818

For: READING CIRCUIT, REFERENCE CIRCUIT,  
AND SEMICONDUCTOR MEMORY DEVICE

Examiner: Not Yet Assigned

REQUEST FOR CORRECTED FILING RECEIPT

MS Missing Parts  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicant, a copy of which is attached hereto, has an error indicated below.

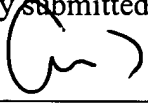
In the Applicant(s) field: please delete "Yamatotakada-Shi" and insert

--Yamatotakada-shi-- in place thereof.

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Dated: November 6, 2003

Respectfully submitted,

By 

Alan S. Hodes

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